

# T112 Introduction

---

## ■Cost Effective Highly Integrated Triple ADC + + 2D Video Decoder + OSD + Scaler + TCON

- Integrates 9-bit Analog to Digital Converters (ADC) & Phase Locked Loop (PLL)
- Scaler supports 2-D adaptive intra-field de-interlacer and non-linear 16:9 aspect ratio.
- Requires no external Frame Buffer Memory for deinterlacer.
- Advanced On Screen Display (OSD) function
- Programmable Timing Controller (Tcon) for Car TV applications
- Multi-standard color decoder with 2D adaptive comb filter
- Innovative and flexible design to reduce total system cost

## Triple 9-bit Analog to Digital Converters (ADC)

### ■27 MSPS Conversion Rate

- Built-in Pre-amp, mid-level & ground clamp circuit
- Automatic Clamp Control for CVBS, Y and C
- Programmable Static Gain Control or Automatic Gain Control for CVBS or Y/C
- Max Input configuration up to 4xCVBS or 2xS-video

## Digital Video Enhancement

### ■Separate Luminance and Chroma Enhancer

- Y Supports Luminance Peaking, DLTI, Black Level Expansion, Contrast and Brightness adjustment
- C Supports DCTI, Saturation and Hue adjustment.

## Advanced Scaling Engine

### ■Two Dimensions FIR Scaler

- Coefficient based sharpness filters
- 2-D edge enhancement
- Independent vertical and horizontal scaling ratio
- 16:9 Non-linear Aspect ratio

### ■LCD Interface

- Provides Gamma correction for panel compensation
- Supports image pan functions
- Programmable Timing Controller
- RGB Triple DAC output

### ■Color Management

- Coef Programmable YCbCr-to-RGB Color Space Converter
- RGB Gamma Correction

### ■Built-in On Screen Display Engine

- 1K-word OSD SRAM memory
- Supports font or bitmap modes
- Supports character blinking, overlay, shadow and border functions
- Fully programmable character mapping
- Supports alpha blending & Zoom-in/Zoom-out function
- Optional fonts can be stored in off-chip serial EEPROM

### ■Versatile VBI Data Decoder

- Supports Close Caption, Wide Screen Signalling and Teletext

### ■Crystal Oscillator Circuit

- Direct interface to a (27.0MHz) Crystal
- Also provide a buffered clock output for external Micro-controller

### ■Digital Test Pattern Generator

- Programmable standard & special panel burn-in test patterns
- Support special border frame blocking mode

### ■Independent Display Phase Lock Loop

- Generates pixel clock output to panel
- Supports free run OSD mode

### ■Serial Bus Interface

- Supports 2-wire (normal speed)

### ■Pulse Width Modulation Outputs

### ■Design For Testability

- Scan chain insertion
- Separated analog & digital test modes

### ■Power Supply: +1.8V & +3.3V & +5V

### ■Package: 64-pin LQFP