

T103 Introduction

■Cost Effective Highly Integrated Triple ADC + ITU656/601 Decoder + 2D Video Decoder + OSD + Scaler + TCON

- Integrates 9-bit Triple Analog to Digital Converters (ADC) & Phase Locked Loop (PLL), for supporting CVBS, S-Video, YPbPr and RGB inputs
- Scaler supports 2-D adaptive intra-field de-interlacer and non-linear 16:9 aspect ration.
- Requires no external Frame Buffer Memory for de-interlace.
- ITU656/601 Decoder with digital input ports for standard ITU656/601 input data
- Digital RGB565/RGB666 input
- Advanced On Screen Display (OSD) function
- Programmable Timing Controller (Tcon) for Car TV applications
- Multi-standard color decoder with 2D adaptive comb filter
- Supports PWM, GPIO, IR, and digital CC/External_OSD interface
- Innovative and flexible design to reduce total system cost

■Triple 9-bits ADCs

- 80MSPS Conversion Rate ADC
- Built-in Pre-amp, mid-level & ground clamp circuit
- Automatic Clamp Control for CVBS, Y and C
- Programmable Static Gain Control or Automatic Gain Control for CVBS or Y/C
- Max Input configuration up to 9xCVBS, 3xS-video
- Build-in Line-Lock PLL for RGB and YPbPr.
- Phases Tracking and Boundary for adjusting input quality.

■Digital Video Enhancement

- Separate Luminance and Chroma Enhancer
- Y Supports Luminance Peaking, DLTI, Black Level Expansion, Contrast and Brightness adjustment
- C Supports DCTI, Saturation and Hue adjustment.

■FIR Based Advanced Scaling Engine

- Coefficient based sharpness filters
- Independent vertical and horizontal scaling ratio
- 16:9 Non-linear Aspect ratio

■LCD Interface

- Provides Gamma correction for panel compensation
- Supports image pan functions
- Programmable Timing Controller
- RGB TTL output or Serial RGB output

■Built-in On Screen Display Engine

- 3K-word OSD SRAM memory
- Supports text or bitmap modes
- Supports character blinking and overlay functions
- Fully programmable character mapping
- Supports alpha blending & Zoom-in/Zoom-out function
- Support shadow/border/window function

■Crystal Oscillator Circuit

- Direct interface to a (27.0MHz) Crystal
- Also provide a buffered clock output for external Micro-controller

■Digital Test Pattern Generator

- Programmable standard & special panel burn-in test patterns
- Support special border frame blocking mode

■Independent Display Phase Lock Loop

- Generates pixel clock output to panel
- Supports free run OSD mode

■Serial Bus Interface

- Supports 2-wire I²C

■3 Pulse Width Modulation Outputs

■4 General Purpose Input Output (GPIO)

■1 IR Decoder (NEC mode + Philips RC5 mode)

■Mixer for Closed Caption or external OSD.

■Design For Testability

- Scan chain insertion
- Separated analog & digital test modes

■Power Supply: +2.5V & +3.3V

■Package: 100-pin PQFP